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| 1 | APPLICATION | FILING or   | GRP ART |               |                |            |            |
|---|-------------|-------------|---------|---------------|----------------|------------|------------|
|   | NUMBER      | 371(c) DATE | UNIT    | FIL FEE REC'D | ATTY.DOCKET.NO | TOT CLAIMS | IND CLAIMS |
| • | 10/598,933  | 09/14/2006  | 1796    | 1130          | PA214WP002     | 6          | 2          |

CONFIRMATION NO. 2213
UPDATED FILING RECEIPT

44719 JOSEPH P. FARRAR 2-4-6 TSUKUDA SUITE 709 CHUO-KU, TOKYO, 104-0051 JAPAN

Date Mailed: 11/03/2008

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#### Applicant(s)

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#### **Assignment For Published Patent Application**

Komatsu Electronic Metals Co., Ltd., Hiratsuka-shi, JAPAN

**Power of Attorney:** The patent practitioners associated with Customer Number <u>44719</u>

# Domestic Priority data as claimed by applicant

This application is a CON of PCT/JP05/07120 04/13/2005

### **Foreign Applications**

JAPAN 2004-117910 04/13/2004

If Required, Foreign Filing License Granted: 06/21/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/598,933** 

**Projected Publication Date:** 02/12/2009

Non-Publication Request: No

Early Publication Request: No

#### Title

Semiconductor wafer inspection device and method

## **Preliminary Class**

505

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